

TS-Q8BL-2729(2927)1H-80DC

100Gb/s 80Km QSFP28 ZR4 BIDI Transceiver module

Product Features

- Up to 80km reach for G.652 SMF
- Cooled 4x25Gb/s LAN WDM Blue or Red Side TOSA, LAN WDM Red or Blue Side ROSA
- 4x25G Electrical Interface
- Single +3.3V power supply
- DDM function implemented
- 2 Wire Serial Interface for module management
- Maximum power dissipation<5.5W
- Operating temperature range: 0℃ ~ 70℃
- Compliant with RoHS6

Applications

- 100GBASE-ZR4 Ethernet
- 100G Datacom& Telecom connections
- 100G 4WDM-40

QSFP28 transceiver modules are designed for use in 100 Gigabit Ethernet links over single mode fiber.

They are compliant with SFF-8665, INF-8438i and IEEE 802.3ba 100GBASE-ZR4. Digital diagnostics functions are available via an I2C interface, as specified by the QSFP28 MSA.

Part No.	Specifications									Application
	Package	Data rate	Laser	Optical Power	Detector	Sensitivity	Temp	Reach	Other	
TS-Q8BL-2729(2927)1H-80DC	QSFP28	Up to 103.1G	EML	1 ~ 7dBm	PIN+SOA	-27dBm	0~70℃	80km	DDM	100GBASE-ZR4

Pin function definitions

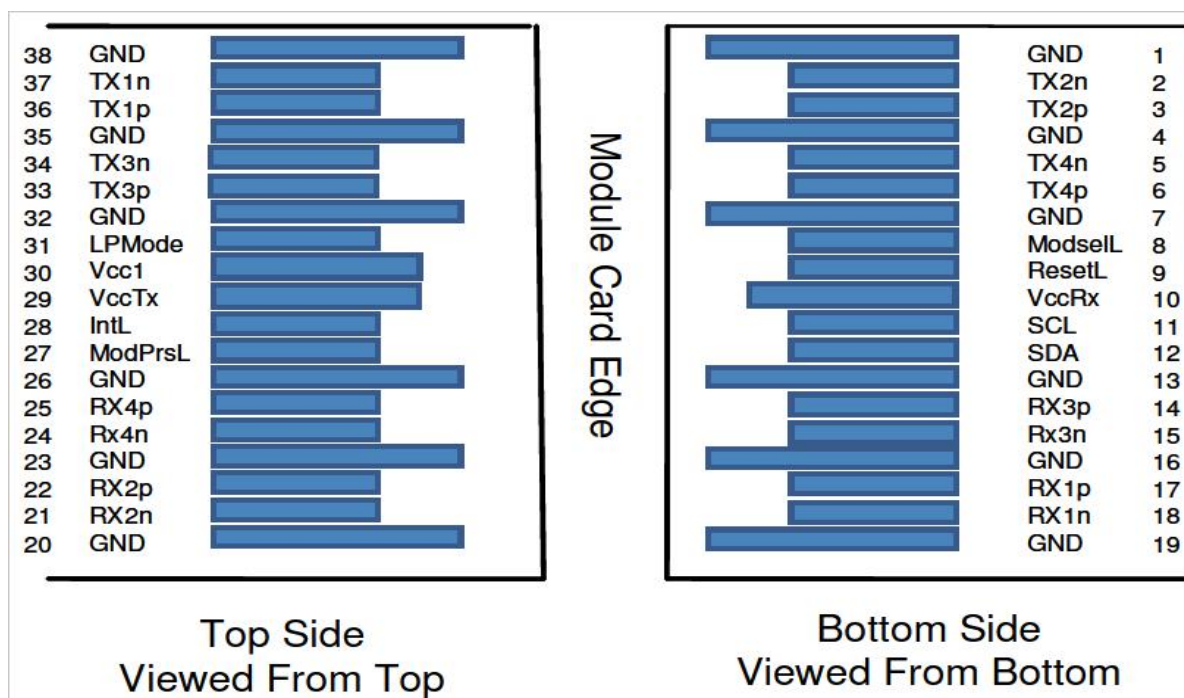


Figure 1. Pin function definitions

Table 1: Transceiver pin descriptions

Pin	Name	Description	Plug Sequence	Notes
1	GND	Ground	1	
2	Tx2n	Transmitter Inverted Data Input	3	
3	Tx2p	Transmitter Non-Inverted Data Input	3	
4	GND	Ground	1	
5	Tx4n	Transmitter Inverted Data Input	3	
6	Tx4p	Transmitter Non-Inverted Data Input	3	
7	GND	Ground	1	
8	ModSelL	Module Select	3	
9	ResetL	Module Reset	3	
10	Vcc Rx	+3.3 V Power supply receiver	2	
11	SCL	2-wire serial interface clock	3	
12	SDA	2-wire serial interface data	3	
13	GND	Ground	1	
14	Rx3p	Receiver Non-Inverted Data Output	3	
15	Rx3n	Receiver Inverted Data Output	3	
16	GND	Ground	1	
17	Rx1p	Receiver Non-Inverted Data Output	3	
18	Rx1n	Receiver Inverted Data Output	3	
19	GND	Ground	1	
20	GND	Ground	1	
21	Rx2n	Receiver Inverted Data Output	3	
22	Rx2p	Receiver Non-Inverted Data Output	3	

23	GND	Ground	1	
24	Rx4n	Receiver Inverted Data Output	3	
25	Rx4p	Receiver Non-Inverted Data Output	3	
26	GND	Ground	1	
27	ModPrsL	Module Present	3	
28	IntL	Interrupt	3	
29	Vcc Tx	+3.3 V Power supply transmitter	2	
30	Vcc1	+3.3 V Power Supply	2	
31	LPMODE	Low Power Mode	3	
32	GND	Ground	1	
33	Tx3p	Transmitter Non-Inverted Data Input	3	
34	Tx3n	Transmitter Inverted Data Input	3	
35	GND	Ground	1	
36	Tx1p	Transmitter Non-Inverted Data Input	3	
37	Tx1n	Transmitter Inverted Data Input	3	
38	GND	Ground	1	

Absolute Maximum Ratings

Parameter	Symbol	Unit	Min	Max
Storage Temperature Range	Ts	°C	-40	85
Relative Humidity	RH	%	0	95
Maximum Supply Voltage	Vcc3	V	-0.5	4.0

Recommended Operating Conditions

Parameter	Symbol	Unit	Min	Typ	Max
Operating Case Temperature Range	Tc	°C	0		70
Power Supply Voltage	Vcc	V	3.14	3.3	3.46
Bit Rate	BR	Gb/s			103.1
Bit Error Ratio	BER				5E-5
Max Supported Link Length	L	Km			80

Electric Ports Definition

Parameter	Symbol	Unit	Min	Typ	Max	Note
Supply Voltage	Vcc	V	3.14	3.3	3.46	
Module Power dissipation		mW			5500	
Transmitter						
Input Differential Impedance	R _{IN}	Ω	90	100	110	
Differential Data Input	V _{IN}	mVp-p	35		900	
Differential input return loss (min)	RL _d	dB	IEEE 802.3ba, Section 83E-5			
Differential to common mode input return loss (min)	RL _{dc}	dB	IEEE 802.3ba, Section			

			83E-6	
Receiver				
Differential Data Output	VOD	mVp-p		900
AC common mode output voltage (RMS)		mV		17.5
Differential output return loss (min)		dB	IEEE 802.3ba, Section 83E-2	
Common to differential mode conversion return loss (min)		dB	IEEE 802.3ba, Section 83E-3	
Output transition time, 20% to 80%		ps	12	
Vertical eye closure		dB		5.5

Optical Characteristics *(T_c=0 °C to 70 °C and V_{cc}= 3.14 to 3.46)*

Parameter	Symbol	Unit	Min	Typ	Max	Notes
Transmitter(per Lane)						
Signaling Speed per Lane		GBd	25.78125 ± 100 ppm			
Blue Side Four Lane Wavelength Range (AQPA0RC80KB-J0KS)	λ1	nm	1272.55	1273.55	1274.54	
	λ2		1276.89	1277.89	1278.89	
	λ3		1281.25	1282.26	1283.27	
	λ4		1285.65	1286.66	1287.68	
Red Side Four Lane Wavelength Range (AQPA0RC80KB-K0JS)	λ5	nm	1294.53	1295.56	1296.59	
	λ6		1299.02	1300.05	1301.09	
	λ7		1303.54	1304.58	1305.63	
	λ8		1308.09	1309.14	1310.19	
Total Average Launch Power	Pout	dBm	7		13	1
Average Launch Power per Lane	TXPx	dBm	1		7	1
Optical Extinction Ratio	ER	dB	6			2
Side-Mode Suppression Ratio (SMSR)	SMSR	dB	30			
Average launch power of OFF transmitter, per lane		dBm			-30	
Relative Intensity Noise	RIN	dB/Hz			-130	
Optical Return Loss Tolerance		dB			20	
Transmitter Reflectance		dB			-12	
Transmitter Eye mask definition {X1,X2,X3, Y1,Y2,Y3}			{0.25,0.4,0.45,0.25,0.28,0.4}			
Receiver(per Lane)						
Signaling Speed per Lane		GBd	25.78125 ± 100 ppm			
Red Side Four Lane Wavelength Range (AQPA0RC80KB-J0KS)	λ5	nm	1294.53	1295.56	1296.59	
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	λ7		1303.54	1304.58	1305.63	
	λ8		1308.09	1309.14	1310.19	

Blue Side Four Lane Wavelength Range (AQPA0RC80KB-K0JS)	$\lambda 1$	nm	1272.55	1273.55	1274.54	
	$\lambda 2$		1276.89	1277.89	1278.89	
	$\lambda 3$		1281.25	1282.26	1283.27	
	$\lambda 4$		1285.65	1286.66	1287.68	
Average Receiver Sensitivity per Lane	Rxsens	dBm			-27	3
Average Received Power per Lane	RXPx	dBm	-27		-6	
Damage Threshold Per Lane	Pmax	dBm	5.5			
Return Loss	RL				-26	
LOS De-Assert	LOSD	dBm			-27	
LOS Assert	LOSA	dBm	-40			
LOS Hysteresis		dB	0.5		6	

Note:

1. The optical power is launched into SMF.
2. Measured with a PRBS $2^{31}-1$ test pattern @25.78125, Hit ratio $\leq 5E-5$.
3. Measured with a PRBS $2^{31}-1$ test pattern @25.78125 Gb/s, BER $\leq 5E-5$.

Typical Application Circuit

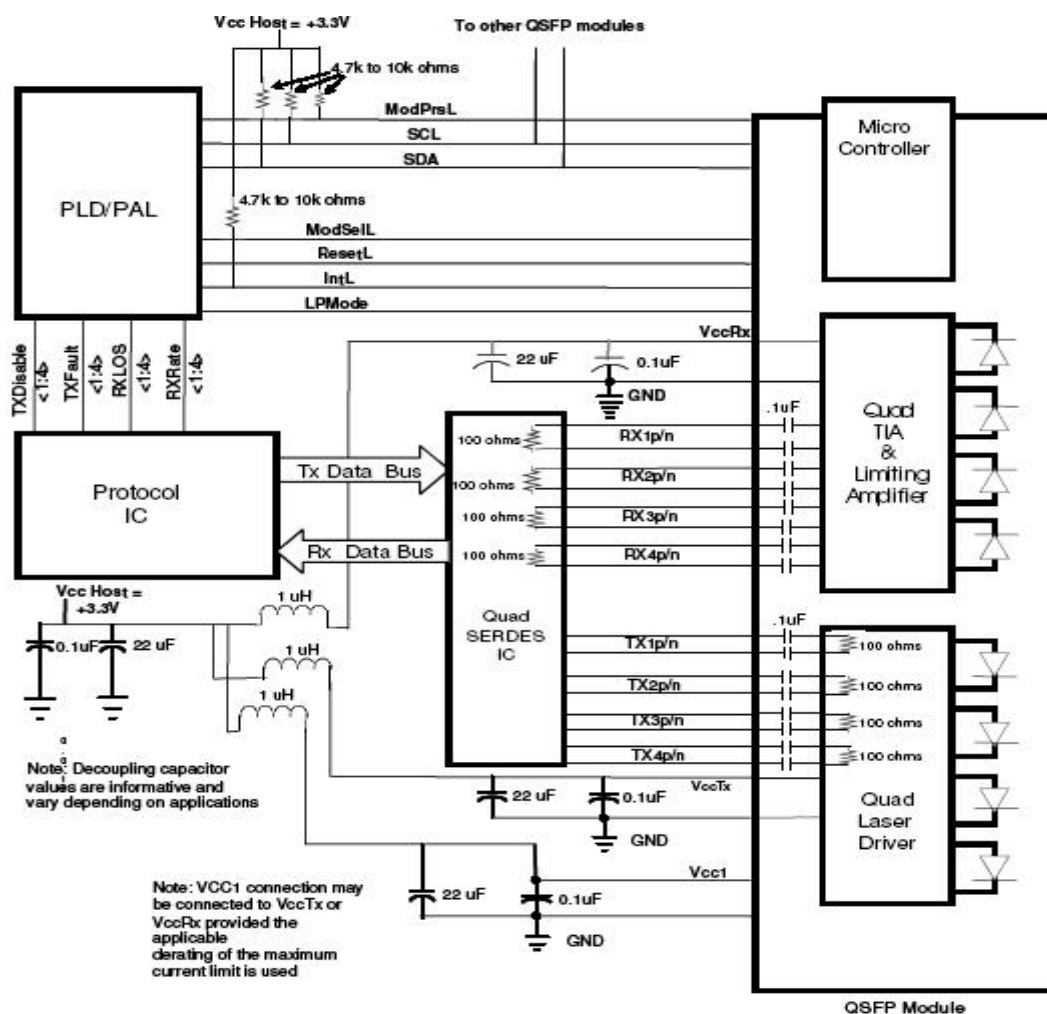


Figure 2. Typical application circuit

Mechanical Dimensions

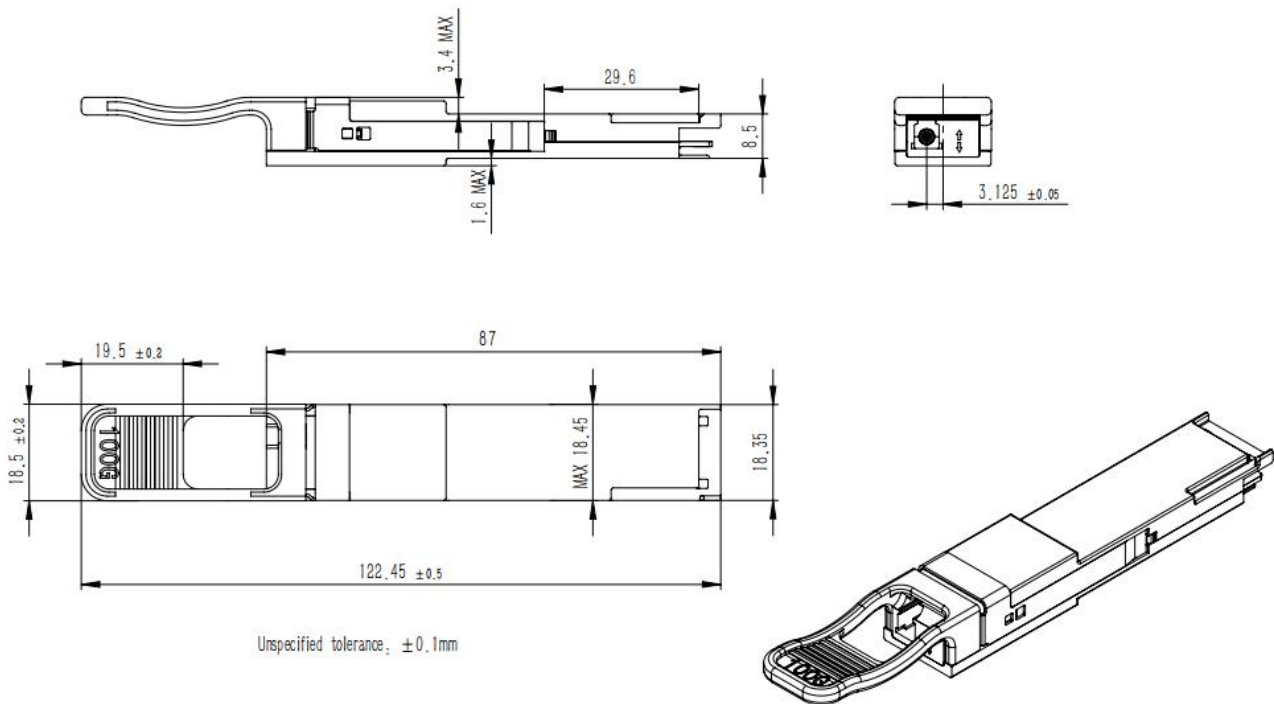


Figure 3. Module Mechanical Dimensions

Digital Diagnostics Functions

As defined by the SFF-8665 Specification for QSFP28 Copper and Optical Transceiver, Our QSFP28 transceivers provide digital diagnostic functions via a 2-wire serial interface, which allows real-time access to the following operating parameters:

- Transceiver temperature
- Laser bias current
- Transmitted optical power
- Received optical power
- Transceiver supply voltage

It also provides a sophisticated system of alarm and warning flags, which may be used to alert end-users when particular operating parameters are outside of a factory-set normal range. The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller (DDTC) inside the transceiver, which is accessed through the 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL pin) is generated by the host.