

TS-XF-8510-02D

10Gb/s 850nm Multimode XFP Optical Transceiver

PRODUCT FEATURES

- Hot-pluggable XFP footprint
- Supports 9.95Gb/s to 11.3Gb/s bit rates
- XFI Loopback Mode
- Power dissipation <1.5W
- RoHS-6 compliant (lead-free)
- Case Temperature range -5°C to 70°C
- Maximum link length of 300m
- Uncooled 850nm VCSEL laser
- Duplex LC connector
- No Reference Clock required
- Built-in digital diagnostic functions
- Standard bail release mechanism

APPLICATIONS

- 10GBASE-SR/SW 10G Ethernet
- 1200-Mx-SN-I 10G Fiber Channel





PRODUCT DESCRIPTION

TRANSCOM's TS-XF-8510-02D Small Form Factor 10Gb/s (XFP) transceivers are compliant with the current XFP Multi-Source Agreement (MSA) Specification. They comply with 10-Gigabit Ethernet 10GBASE-SR/SW per IEEE 802.3ae and 10G Fiber Channel 1200-Mx-SN-I.Digital diagnostics functions are available via a 2-wire serial interface, as specified in the XFP MSA.

I. Absolute Maximum Ratings

Parameter Symbol		Min	Тур	Max	Unit	Ref.
Maximum Supply Voltage	Vcc3	-0.5		4.0	V	
Storage Temperature	T_{s}	-40		85	°C	
Case Operating Temperature	T _{case}	-5		70	°C	

II. Electrical Characteristics ($T_{OP} = -5 \text{ to } 70 \text{ °C}$, $V_{CC3} = 3.13 \text{ to } 3.45 \text{ Volts}$)

Parameter	Symbol	Min	Тур	Max	Unit	Ref.
Supply Voltage	Vcc3	3.13		3.45	V	
Supply Current	Icc3			350	mA	
Module total power	P			1.5	W	1
Transmitter						
Input differential impedance	$R_{_{ m in}}$		100		Ω	2
Differential data input swing	Vin,pp	120		1000	mV	
Transmit Disable Voltage	${ m V}_{_{ m D}}$	2.0		Vcc	V	3
Transmit Enable Voltage	$ m V_{_{EN}}$	GND		GND+ 0.8	V	
Transmit Disable Assert Time				10	us	
Receiver						
Differential data output swing	Vout,pp	600	650	800	mV	4
Data output rise time	t r			40	ps	5
Data output fall time	$\mathfrak{t}_{_{\mathrm{f}}}$			40	ps	5
LOS Fault	V LOS fault	Vcc - 0.5		Vcc _{HOST}	V	6
LOS Normal	V LOS norm	GND		GND+0.5	V	6
Power Supply Rejection	PSR					7

Notes:

- 1. Maximum total power value is specified across the full temperature and voltage range.
- 2. After internal AC coupling.
- 3. Or open circuit.
- 4. Into 100 ohms differential termination.
- 5. These are unfiltered 20-80% values
- 6. Loss Of Signal is open collector to be pulled up with a $4.7k\Omega 10k\Omega$ resistor to 3.15 3.6V. Logic 0 indicates normal operation; logic 1 indicates no signal detected.
- 7. Per Section 2.7.1. in the XFP MSA Specification.



III. Optical Characteristics ($T_{OP} = -5 \text{ to } 70 \text{ }^{\circ}\text{C}, V_{CC3} = 3.13 \text{ to } 3.45 \text{ Volts}$)

Parameter	Symbol	Min	Тур	Max	Unit	Ref.
Transmitter						
Average Optical Power	${ m P}_{ m AVE}$	-6		-1.0		1
Optical Wavelength	λ	840	850	860	nm	
Optical Extinction Ratio	ER	3.0	5		dB	
Transmitter and Dispersion Penalty	TDP			3.9	dB	
Average Launch power of transmitter	P_{OFF}			-30	dBm	
Tx Jitter		Tx_{j}			Per 802.3ae requirements	
Encircled Flux	<4.5μm			30	%	2
Relative Intensity Noise	RIN ₁₂ OMA			-128	dB/Hz	
Receiver						
Receiver Sensitivity@ 10.5Gb/s	R _{SENS}			-10	dBm	
Input Saturation Power (Overload)	Psat	+0.5			dBm	
Wavelength Range	$\lambda_{_{ m C}}$	840		860	nm	
Receiver Reflectance	R			-12	dB	
LOS De-Assert	LOS _D			-12	dBm	
LOS Assert	LOS_{A}	-30			dBm	
LOS Hysteresis		0.5			dB	

Notes:

- 1. Average power figures are informative only, per IEEE 802.3ae.
- 2. Measured into Type A1a (50/125 μm multimode) fiber per ANSI/TIA/EIA-455-203-2.

IV. Pin Assignment

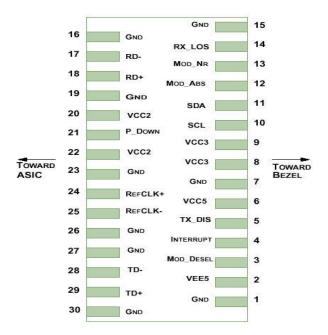


Diagram of Host Board Connector Block Pin Numbers and Name



1	Pin	Logic	Symbol	Name/Description	
LVTTL-I	1		GND	Module Ground	
1	2		VEE5	Optional –5.2 Power Supply – Not required	
Serial 2-wire interface 2	3	LVTTL-I	Mod-Desel	,	
Serial S	4	LVTTL-O	Interrupt		2
The series of the series of the series of the module of the series of the series of the module of the series of	5	LVTTL-I	TX_DIS	Transmitter Disable; Transmitter laser source turned off	
NCC3	6		VCC5	+5 Power Supply – Not required	
9	7		GND	Module Ground	1
10	8		VCC3	+3.3V Power Supply	
11 LVTTL-I/O SDA Serial 2-wire interface data line 2 12 LVTTL-O Mod_Abs Module Absent; Indicates module is not present. Grounded in the module. 2 13 LVTTL-O Mod_NR Module Not Ready; TRANSCOM's defines it as a logical OR between RX_LOS and Loss of Lock in TX/RX. 2 14 LVTTL-O RX_LOS Receiver Loss of Signal indicator 2 15 GND Module Ground 1 16 GND Module Ground 1 17 CML-O RD- Receiver inverted data output 18 CML-O RD+ Receiver non-inverted data output 19 GND Module Ground 1 20 VCC2 +1.8V Power Supply - Not required POWER Down/R Power Down; When high, places the module in the low power stand-by mode and on the falling edge initiates a complete reset of the module including the 2-wire serial interface, equivalent to a power cycle. 22 VCC2 +1.8V Power Supply - Not required 23 GND Module Ground 1 24 PECL-I RefCLK+ Reference Clock inver	9		VCC3	+3.3V Power Supply	
12	10	LVTTL-I	SCL	Serial 2-wire interface clock	
13	11	LVTTL-I/O	SDA	Serial 2-wire interface data line	2
13	12	LVTTL-O	Mod_Abs		2
15	13	LVTTL-O	Mod_NR		2
16 GND Module Ground 1 17 CML-O RD- Receiver inverted data output	14	LVTTL-O	RX_LOS	Receiver Loss of Signal indicator	
17	15		GND	Module Ground	1
Receiver non-inverted data output 19	16		GND	Module Ground	1
19	17	CML-O	RD-	Receiver inverted data output	
VCC2	18	CML-O	RD+	Receiver non-inverted data output	
LVTTL-I	19		GND	Module Ground	
LVTTL-I P_Down/R ST mode and on the falling edge of P_Down initiates a module reset	20		VCC2	+1.8V Power Supply – Not required	
Reset; The falling edge initiates a complete reset of the module including the 2-wire serial interface, equivalent to a power cycle. VCC2 +1.8V Power Supply – Not required GND Module Ground 1 RefCLK+ RefCLK+ Reference Clock non-inverted input, AC coupled on the host board – Not required RefCLK- Reference Clock inverted input, AC coupled on the host board – Not required GND Module Ground 1 GND Module Ground 1 GND Module Ground 1 Transmitter inverted data input CML-I TD- Transmitter inverted data input	21	I WTTI I	P_Down/R	Power Down; When high, places the module in the low power stand-by	
23 GND Module Ground 1	21	LVIIL-I	ST		
24PECL-IRefCLK+Reference Clock non-inverted input, AC coupled on the host board - Not required325PECL-IRefCLK-Reference Clock inverted input, AC coupled on the host board - Not required326GNDModule Ground127GNDModule Ground128CML-ITD-Transmitter inverted data input29CML-ITD+Transmitter non-inverted data input	22		VCC2	+1.8V Power Supply – Not required	
Not required 3 Not required 3	23		GND	Module Ground	1
26 GND Module Ground 1 27 GND Module Ground 1 28 CML-I TD- Transmitter inverted data input 29 CML-I TD+ Transmitter non-inverted data input	24	PECL-I	RefCLK+	Reference Clock non-inverted input, AC coupled on the host board –	
26GNDModule Ground127GNDModule Ground128CML-ITD-Transmitter inverted data input29CML-ITD+Transmitter non-inverted data input	25	PECL-I			3
28 CML-I TD- Transmitter inverted data input 29 CML-I TD+ Transmitter non-inverted data input	26		GND	Module Ground	1
29 CML-I TD+ Transmitter non-inverted data input	27		GND	Module Ground	1
1	28	CML-I	TD-	Transmitter inverted data input	
30 GND Module Ground 1	29	CML-I	TD+	Transmitter non-inverted data input	
	30		GND	Module Ground	1

Notes:

- 1. Module circuit ground is isolated from module chassis ground within the module.
- 2. Open collector; should be pulled up with $4.7k\Omega 10k\Omega$ on host board to a voltage between 3.15V and 3.6V.
- 3. A Reference Clock input is not required by the TS-XF-8510-02D. If present, it will be ignored.

V. General Specifications



Parameter		Symbol	Min	Тур	Max	Units	Ref.
E	Bit Rate	BR	9.95		11.3	Gb/s	1
Bit I	Error Ratio	BER			10 ⁻¹²		2
Maximum Supported Distances							
Fiber Type	850nm OFL Bandwidth						
62.5	160MHz-km	Lmax			26	***	
62.5μm	OM1 500MHz-km		Liliax			33	m
	400MHz-km				66		
50μm	OM2 500MHz-km	Lmax			82	m	
	OM3 2000MHz-km				300		

Notes:

- 1. 10GBASE-SR/SW, 1200-Mx-SN-I
- 2. Tested with 10.3Gbps, $2^{31} 1$ PRBS

VI. Digital Diagnostic Functions

As defined by the XFP MSA¹, TRANSCOM XFP transceivers provide digital diagnostic functions via a 2-wire serial interface, which allows real-time access to the following operating parameters:

- Transceiver temperature
- Laser bias current
- Transmitted optical power
- Received optical power
- Transceiver supply voltage

It also provides a sophisticated system of alarm and warning flags, which may be used to alert end-users when particular operating parameters are outside of a factory-set normal range.

The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller (DDTC) inside the transceiver, which is accessed through the 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL pin) is generated by the host. The positive edge clocks data into the XFP transceiver into those segments of its memory map that are not write-protected. The negative edge clocks data from the XFP transceiver. The serial data signal (SDA pin) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially. The 2-wire serial interface provides sequential or random access to the 8 bit parameters, addressed from 000h to the maximum address of the memory.

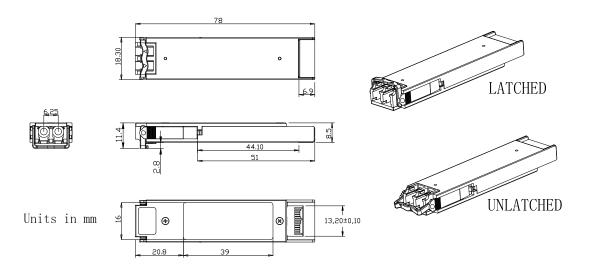
For more detailed information including memory map definitions, please see TRANSCOM's



Application Note AN-2035 "Digital Diagnostic Monitoring Interface for XFP Optical Transceivers", or the XFP MSA Specification1.

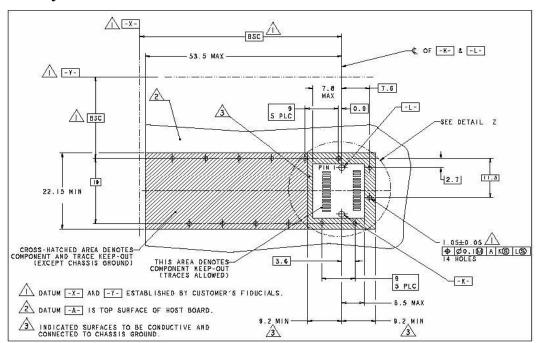
VII. Mechanical Specifications

TRANSCOM's XFP transceivers are compliant with the dimensions defined by the XFP Multi-Sourcing Agreement (MSA). Bail color is beige.



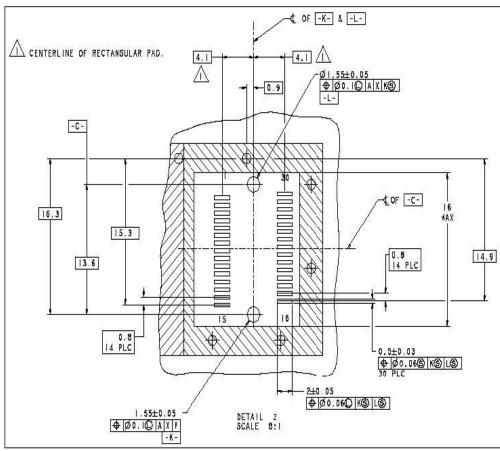
XFP Transceiver (dimensions are in mm)

VIII. PCB Layout and Bezel Recommendations

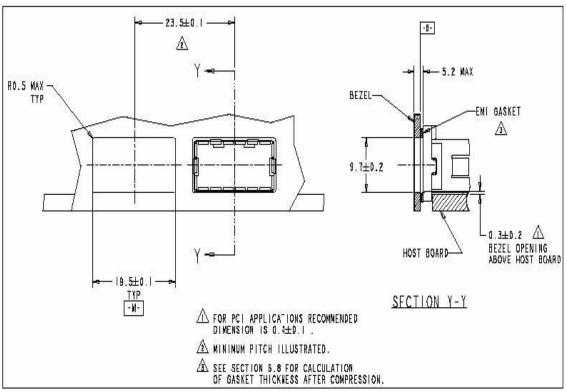


XFP Host Board Mechanical Layout (dimensions are in mm)





XFP Detail Host Board Mechanical Layout (dimensions are in mm)



XFP Recommended Bezel Design (dimensions are in mm)



IX. Regulatory Compliance

Feature	Reference	Performance	
Electrostatic discharge (ESD)	IEC/EN 61000-4-2	Compatible with standards	
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN 55022 Class B (CISPR 22A)	Compatible with standards	
Laser Eye Safety	FDA 21CFR 1040.10, 1040.11 IEC/EN 60825-1, 2	Class 1 laser product	
Component Recognition	IEC/EN 60950 ,UL	Compatible with standards	
ROHS	2002/95/EC	Compatible with standards	
EMC	EN61000-3	Compatible with standards	

Appendix A. Document Revision

Version No.	Date	Description	
1.0	2010-09-01	Preliminary datasheet	
2.0	2011-09-10	Update format and company's logo	